



W (Three-Phase) = 5 A X (Three-Phase) = 5 A Y (Neutral x 3) = 1 A, 1 A, 1 A	4
W (Three-Phase) = 1 A X (Three-Phase) = 1 A Y (Neutral x 3) = 5 A, 5 A, 5 A	5
W (Three-Phase) = 1 A X (Three-Phase) = 1 A Y (Neutral x 3) = 5 A, 5 A, 1 A	6
W (Three-Phase) = 1 A X (Three-Phase) = 1 A Y (Neutral x 3) = 5 A, 1 A, 1 A	7
W (Three-Phase) = 1 A X (Three-Phase) = 1 A Y (Neutral x 3) = 1 A, 1 A, 1 A	8

### Ethernet Communications Protocols

None	X X
FTP, Telnet, and DNP3 LAN/WAN, PRP*	B
FTP, Telnet, DNP3 LAN/WAN, PRP and IEC 61850*	C

### Ethernet Connection Options

None	X X
Ports 5A, 5B <sup>(6)</sup> With PTP: Ethernet Card With Two 10/100BASE-T Connectors*	1
Ports 5A, 5B <sup>(6)</sup> With PTP: Ethernet Card With Two 100BASE-FX Connectors*	3
Ports 5A, 5B <sup>(6)</sup> With PTP: Ethernet Card With One 10/100BASE-T, One 100BASE-FX Connector*	5
Ports 5C, 5D <sup>(7)</sup> : Ethernet Card With Two 10/100BASE-T Connectors*	0
Ports 5C, 5D <sup>(7)</sup> : Ethernet Card With Two 100BASE-FX Connectors*	2
Ports 5C, 5D <sup>(7)</sup> : Ethernet Card With One 10/100BASE-T, One 100BASE-FX Connector*	4

### Mainboard Input Voltage

24 Vdc	1
48 Vdc	2
110 Vdc	3
125 Vdc	4
220 Vdc	5
250 Vdc	6

### Mounting

Horizontal Rack Mount	H
Horizontal Panel Mount	3
Vertical Rack Mount	V 5 X X X X X
Vertical Panel Mount	4 5 X X X X X

### Chassis

5U, No Additional I/O Board	5 X X X X X
6U, Up to One Additional I/O Board	6 X X X
7U, Up to Two Additional I/O Boards	7 X
9U, Up to Four Additional I/O Boards	9

### I/O Board Position B for 6U, 7U or 9U Chassis

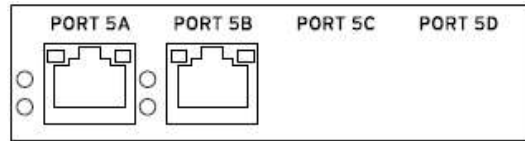
Empty I/O Board Position	0 X X X X
8 Optoisolated Independent Level-Sensitive Inputs, 13 Standard Form A, 2 Standard Form C Outputs*	2
24 Optoisolated Level-Sensitive Inputs, 8 Outputs <sup>(8)</sup> *	4



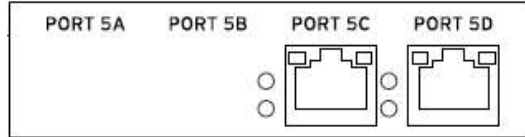
(4) Current Channel Y is comprised of three single-phase neutral current inputs.

(5) When using the Y current channels for synchrophasor data, all Y channel currents must have the same nominal current rating (1 A, 1 A, 1 A or 5 A, 5 A, 5 A) to provide proper synchrophasor scaling. **AC Input Card 2 Configuration** options 1, 4, 5, or 8 meet this criteria.

(6) Only two Ethernet ports will be populated based on the Ethernet Connection Options Selection. IEEE Precision Time Protocol (PTP) is only available on Port 5A and 5B.



(7) PTP is not available on Ports 5C and 5D.



(8) The 24 Optoisolated Inputs are comprised of 18 Common Inputs and 6 Independent Inputs. The 8 outputs are comprised of 6 High-Speed, High-Current Interrupting Outputs and 2 Standard Outputs.

(9) This product comes standard with a CD manual. One complimentary instruction manual kit is available upon request with each product purchased.

(10) Order a Connectorized® Wiring Harness for SEL-487E (harness shipped separately).

*Making Electric Power Safer, More Reliable, and More Economical®*

**SEL** SCHWEITZER ENGINEERING LABORATORIES, INC.

2350 NE Hopkins Court - Pullman, WA 99163 USA  
Phone: +1.509.332.1890 - Fax: +1.509.332.7990